

Notice of References Cited

Application/Control No.

09/754,325

Applicant(s)/Patent Under
Reexamination
YAMASHITA ET AL.

Examiner

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Art Unit

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5843813 /	12-1998	Wei et al.	438/200
	B	US-6384455 /	05-2002	Nishigohri	257/371
	C	US-5519243 /	05-1996	Kikuda et al.	257/296
	D	US-3244949 /	03-1962	Hilbiber	257/577
	E	US-5629545 /	05-1997	Leach	257/362
	F	US-4426766 /	01-1984	Lee	257/376
	G	US-5622876 /	04-1997	Zambrano et al.	438/135
	H	US-6066520 /	05-2000	Suzuki	438/202
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	59-205751 /	11-1984	Jp	Miyazaki	257/371
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, Silicon processing for the VLSI era, 1995, Lattice press, Vol. 3, page 523. /
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.